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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yusuke Igarashi et al. Art Unit : Unknown
Serial No. : 10/824,966 Examiner : Unknown
Filed : April 14, 2004
Title : A METHOD FOR FABRICATING A CIRCUIT DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Please correct the Filing Receipt for the above-referenced application. The name of the third inventor is Yoshiyuki KOBAYASHI.

Please supply a corrected Filing Receipt to the undersigned with respect to this application. A copy of the original Filing Receipt showing the desired changes in red ink is attached for your convenience.

No fee is believed to be due. If, however, there are any charges or credits, please apply them to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7/7/04

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10/824,966	04/14/2004	1765	788	14225-005002	9	21	1

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Applicant(s)

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Assignment For Published Patent Application

Sanyo Electric Co., Ltd., an Osaka Japan Corporation;

Docketed By	Practice Systems
Action Code:	Correct Filing Receipt
Base Date:	06-28-04
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Domestic Priority data as claimed by applicant

This application is a CON of 10/172,415 06/14/2002

Foreign Applications

JAPAN P. 2001-185423 06/19/2001

Docketed By	Billing Secretary
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Projected Publication Date: 10/07/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method for fabricating a circuit device

Preliminary Class

438



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